IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

ABE, Yoshiyuki

Serial No.:

Not yet assigned

Filed:

October 21, 2003

For:

FABRICATION METHOD OF SEMICONDUCTOR CIRCUIT DEVICE

<u>INFORMATION DISCLOSURE STATEMENT</u> <u>UNDER 37 CFR 1.97 & 1.98</u>

Mail Stop DD Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

October 21, 2003

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted with the new application.

Although the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR 1.98(a)(3) for a concise explanation of the relevance is satisfied by the discussion of the documents in the specification, for example, on pages one (1) and two (2) and the English abstracts.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (Case: 501.43182X00), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STQUT & KRAUS LLP

William I. Solomon

Registration No. 28,565

WIS/alb (703) 312-6600 Attachments

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